EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	13155	709/250 709/318 709/250 709/318 713/323 713/322 713/324 713/600 713/500 713/156	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2008/01/01 13:49
L2	184	((rate traffic) near2 controller$1) with (increa$4 adjust$3 chang$3) with "more"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2008/01/01 13:49
L3	184	L2	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2008/01/01 13:50
L4	1	1 and 2	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2008/01/01 13:50
S1	27	"5673253"	USPAT	OR	OFF	2005/08/22 08:41
S2	6	"6091729"	USPAT	OR	OFF	2005/08/22 08:42
S3	16	"6157643"	USPAT	OR	OFF	2005/08/22 08:42
S4	3	"6356548"	USPAT	OR	OFF	2005/08/22 08:42
S5	0	"6091729".pn. and memory	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/08/22 08:49
S6	1	"6091729".pn. and buffer	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/08/22 08:50
S7	0	"6091729".pn. and buffer and FIFo	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/08/22 08:50

EAST Search History

S8	0	"6091729".pn. and buffer and FIFO	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/08/22 08:50
S9	1	"6091729".pn. and buffer and rates	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/08/22 08:50
S10	1047817	(control near6 (signal message indicators))	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/08/22 09:06
S11	164	(first adj rate) same (stop cease pause terminate) same (second adj rate)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/08/22 09:12
S12	75	S11 and S10	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/08/22 09:08
S13	23	S11 same S10	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/08/22 09:34
S14	2388329	compare$3 evaluat$3 estimate$3	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/08/22 09:08
S15	13790	S14 same rate same predetermin$3 same value	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/08/22 09:10
S16	345026	S14 same (first previous) same (second latest last)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/08/22 09:11
S17	5942	S15 and S16	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/08/22 09:11

EAST Search History

S18	2	S17 and S13	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/08/22 09:12
S19	12	"6122251"	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/08/22 10:02
S20	1090	(first adj rate) near6 (greater less) near6 (second adj rate)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/08/22 10:04
S21	73	S20 and (count$5 same (number quality) same output)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/08/22 13:28
S22	65	S21 and (@ad<"20011016" @rlad<"20011016")	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2007/12/31 16:12
S23	227767	(value count number quality) same data same (represent$3 resembl$3) same (value number quality count) same data	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/08/22 13:32
S24	71170	S23 same (FIFO buffer memory storage)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/08/22 13:33
S25	29594	"3" same (compar$3 evaluat$3) same (threshold predetermine)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/08/22 13:33
S26	1175	S24 same (compar$3 evaluat$3) same (threshold predetermine)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/08/22 13:34
S27	110	S26 same switch	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2005/08/22 13:34

EAST Search History

S28	49	S27 and (@ad<"20011016" @rlad<"20011016")	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/01/11 13:24
S29	9	"6147997"	USPAT	OR	OFF	2006/01/10 12:23
S30	15	"6122251"	USPAT	OR	OFF	2006/01/10 15:14
S31	29	"5673253"	USPAT	OR	OFF	2006/01/10 15:18
S32	7	"6091729"	USPAT	OR	OFF	2006/01/10 15:18
S33	17	"6157643"	USPAT	OR	OFF	2006/01/10 15:19
S34	3	"6356548"	USPAT	OR	OFF	2006/01/10 15:47
S35	948	(rate near10 control$3) same FIFO	USPAT	OR	OFF	2006/01/10 15:48
S36	0	(rate near10 control$3) same FIFO same (line adj card)	USPAT	OR	OFF	2006/01/10 15:48
S37	0	(dyanmic near10 rate near10 control$3) same FIFO	USPAT	OR	OFF	2006/01/10 15:48
S38	0	(dyanmic near10 rate near10 control$3) same FIFO	USPAT	OR	ON	2006/01/10 15:48
S39	1024	(rate near10 control$3) same FIFO	USPAT	OR	ON	2006/01/10 15:50
S40	0	S39 and (buffer same count$3 same input) and (buffer same count3$3 same output)	USPAT	OR	ON	2006/01/10 15:50
S41	0	S39 and (count$3 same input) and (count3$3 same output)	USPAT	OR	ON	2006/01/10 15:50
S42	199	S39 and (adjust$3 near10 rate)	USPAT	OR	ON	2006/01/10 15:50
S43	199	S42 and (adjust$3 near10 rate)	USPAT	OR	ON	2006/01/10 15:54
S44	25	S42 and (adjust$3 near10 rate) and (congest$4 near10 control$3)	USPAT	OR	ON	2006/01/10 15:55
S45	12	"6510153"	USPAT	OR	OFF	2006/01/11 10:35
S46	4384	DHCP	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/01/11 10:35
S47	0	S46 and (wir$3 same pool) and ((wire adj less) same pool) and ((hand adj off) same pool)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/01/11 10:37
S48	19	S46 and (wir$3 same pool)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/01/11 10:37

EAST Search History

S49	0	S46 and (wir$3 same pool) and ((wire adj less) same pool)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/01/11 10:37
S50	0	S46 and((wire adj less) same pool)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/01/11 10:37
S51	70	S46 and ((wireless) same pool)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/01/11 10:37
S52	15	S46 and (wir$3 same pool) and ((wireless) same pool)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/01/11 10:37
S53	2	S52 and (handoff same pool)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/01/11 10:38
S54	0	S53 and (@ad<"20011016" @rlad<"20011016")	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/01/11 10:38
S55	8	S52 and (@ad<"20011016" @rlad<"20011016")	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/01/11 13:21
S56	5	DHCP and (time near10 sensitive) and (time near10 insensitive)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/01/11 13:25
S57	3	S56 and (@ad<"20011016" @rlad<"20011016")	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/01/11 13:42
S58	149	DHCP and (time near10 sensi$5)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/01/11 13:38

EAST Search History

S59	63636	(allocat$3 dedicat$3 assign$3) near10 address$2	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/01/11 13:39
S60	112040	(priorit$4 sensit$6) near10 time	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/01/11 13:49
S61	247	S59 and S60 and DHCP	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/01/11 13:42
S62	178	S61 and (@ad<"20011016" @rlad<"20011016")	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/03/21 14:40
S63	105	S62 not S58	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/01/11 13:43
S64	1	"20030079017" and time	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/01/11 13:46
S65	1066	(priorit$4 sensit$6) near10 (S59)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/01/11 13:49
S66	37	S65 and DHCP	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/01/11 13:49
S67	24	S66 and (@ad<"20011016" @rlad<"20011016")	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/01/11 13:49
S68	1	"20030079017" and time	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/03/21 14:41

EAST Search History

S69	0	S68 and Aimoto.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/03/21 14:42
S70	534	Aimoto.in.	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/03/21 14:42
S71	468	Aimoto.in. and (@ad<"20011016" @rlad<"20011016")	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/03/21 14:43
S72	25	S71 and FIFO	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/03/21 14:43
S73	231992	rate near5 control	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/15 13:44
S74	4549	priority near5 buffer	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/15 13:45
S75	596	S73 and S74	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/15 13:45
S76	429	S75 and (@ad<"20011016" @rlad<"20011016")	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/15 13:45
S77	187	S76 and FIFO	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/16 09:38
S78	187	S77	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/15 13:47

EAST Search History

S79	99	S77 and "370"/	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/15 13:48
S80	1	("0000370").PN.	US-PGPUB; USPAT	OR	OFF	2006/05/15 13:48
S81	9	"6147997"	USPAT	OR	OFF	2006/05/16 09:02
S82	746	high$2 near2 priority near2 (buffer$1 FIFO)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/16 09:41
S83	469	low$2 near2 priority near2 (buffer$1 FIFO)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/16 09:41
S84	237	S82 same S83	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/16 09:40
S85	2	S84 same (rate near2 control$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/16 09:40
S86	2	S85 and (@ad<"20011016" @rlad<"20011016")	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/16 09:41
S87	294	S82 and S83	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/16 09:40
S88	233	S87 and (@ad<"20011016" @rlad<"20011016")	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/16 10:36
S89	131	low$2 near2 priority near2 (FIFO)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/16 09:41

EAST Search History

S90	177	high$2 near2 priority near2 (FIFO)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/16 09:41
S91	79	S89 same S90	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/16 09:41
S92	61	S91 and (@ad<"20011016" @rlad<"20011016")	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/16 10:31
S93	27222	(rate near5 control$3) near10 (chang$3 tuggle)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/16 10:37
S94	2	S93 same (((first previous prior) near5 rate) near10 stop near10 (second near5 rate))	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/16 10:36
S95	1	S94 and (@ad<"20011016" @rlad<"20011016")	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/16 10:46
S96	29284	(rate near5 control$3) near10 (chang$3 tuggle alter$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/16 10:45
S97	1438	(rate near5 control$3) near10 (transmit$5 near10 data) near10 (transmit$5 near5 rate)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/16 10:46
S98	956	S97 and (@ad<"20011016" @rlad<"20011016")	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/16 10:47
S99	146	S98 and (FIFO)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/16 10:47

EAST Search History

S100	8	S98 and (FIFO) and (switch$3 near4 fabric)	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/16 16:00
S101	3727	370/229	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/16 16:00
S102	4708	370/235	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/16 16:00
S103	4160	370/412	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2006/05/16 16:00
S104	1	"20030016686"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2006/09/06 10:43
S105	1	"6118761".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2006/09/06 10:43
S106	12351295	@ad<"20011016" @rlad<"20011016"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2006/11/14 14:33
S107	2	"20020057712"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2006/11/14 14:33
S108	2	S107 and S106	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2006/11/14 14:35

EAST Search History

S109	25665	rate near5 control near5 signal	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2006/11/14 15:51
S110	19025	S109 and S106	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2006/11/14 14:47
S111	47	S110 and (switch$3 near5 fabric)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2006/11/14 15:54
S112	23	S110 and ((switch$3 near5 fabric) same control)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2006/11/14 14:48
S113	11	S110 and ((switch$3 near5 fabric) same control) and threshold$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2006/11/14 14:57
S114	3	S113 and FIFO	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2006/11/14 14:58
S115	2875	data near5 rate near5 control near5 signal	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2006/11/14 15:52
S116	2139	S115 and S106	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2006/11/14 15:54

EAST Search History

S117	10	S116 and (switch$3 near5 fabric)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2006/11/14 15:57
S118	3	S116 and (switch$3 near5 fabric) and threshold	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2006/11/14 16:30
S119	1	"6118761".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2006/11/14 17:43
S120	2	S116 and (switch$3 near5 fabric) and (compar$3 near5 rate$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2006/11/14 16:32
S121	270	S116 and (compar$3 near5 rate$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2006/11/14 16:34
S122	5	S121 and (line near5 card$1)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2006/11/14 16:33
S123	62	S121 and "370"/$.ccls.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2006/11/14 16:34
S124	5275	370/468	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2006/11/14 17:43

EAST Search History

S125	2	"6118761".pn. "20020048280"	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2007/04/12 15:34
S126	5610	370/468	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2007/04/13 16:10
S127	25	"5539747".pn. "5673253".pn. "6091729".pn. "6122251".pn. "6147997".pn. "6157643".pn. "6356548".pn. "6094692".pn. "6167030".pn. "6700869".pn. "6715007".pn. "6732209".pn. "6839768".pn. "6937568".pn. "7068602".pn. "7072299".pn. "7145904".pn. "7298703".pn. "20030007452" "20030035373" "20030128703" "20030165150" "20030231593" "20040004961"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2007/12/29 16:13
S128	1	"5515359".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2007/12/29 17:26
S129	180	((rate traffic) near2 controller$1) with (increa$4) with ("times")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2007/12/29 17:28
S130	12427082	@ad<"20011016" @rlad<"20011016"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2007/12/29 17:27
S131	122	S129 and S130	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2007/12/29 17:28

EAST Search History

S132	0	((rate traffic) near2 controller$1) with (increa$4) with mutiple with ("times")	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2007/12/29 17:29
S133	1209	((rate traffic) near2 controller$1) with (increa$4)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2007/12/29 17:30
S134	701	S133 and S130	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2007/12/29 17:29
S135	184	((rate traffic) near2 controller$1) with (increa$4 adjust$3 chang$3) with "more"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2007/12/29 17:31
S136	94	S135 and S130	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2007/12/29 17:37
S137	2	"20030058880"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2007/12/29 17:45
S138	10	"20030058880" "20030202510" "7042842".pn. "20030126297" "6944168".pn. "6654373".pn. "20030074473" "5612953".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2007/12/29 17:39
S139	1	"20030231593"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2007/12/29 17:46

EAST Search History

S140	0	"6798743".pn. and rate$1	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2007/12/29 17:46
S141	0	"6798743".pn. and rate	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2007/12/29 17:47
S142	1	"6385168".pn.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2007/12/29 18:23
S143	1	"6118761".pn. and FIFO	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2007/12/29 18:23
S144	0	"2006024258"	US-PGPUB; USPAT	OR	OFF	2007/12/30 14:13
S145	1	"20060242585"	US-PGPUB; USPAT	OR	OFF	2007/12/30 15:50
S146	5	"IEEE1394.a"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2007/12/30 16:39
S147	1	"20050188232"	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2007/12/30 16:40
S148	1	"Network interface card accessible during low power consumption mode".ti.	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2007/12/30 16:41

EAST Search History

S149	14	("5388265" \| "5404544" \| "5671355" \| "5689714" \| "5802305" \| "5938771" \| "5983353" \| "6021493" \| "6324652" \| "6393570").PN. OR ("6513128").URPN.	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/30 17:07
S150	1	"6193422".pn. and interface	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/30 17:24
S151	1	"6816976".pn.	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/30 17:24
S152	1	"6816976".pn. and interface	US-PGPUB; USPAT; USOCR	OR	ON	2007/12/30 17:24
S153	1	(reduc$3 with power near2 comsumption) same printer$3 same (devic$2 computer$1 pda)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2007/12/31 16:12
S154	246	network with interface with (power near2 consumption)	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; IBM_TDB	OR	ON	2007/12/31 16:12
S155	12331332	(@ad<"20011016" @rlad<"20011016")	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2007/12/31 16:27
S156	0	S153 and S154	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2007/12/31 16:13
S157	91	S155 and S154	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2007/12/31 16:14
S158	30	S157 and network and storage	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2007/12/31 16:17

EAST Search History

S159	11	S157 and network and storage and printer	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2007/12/31 16:26
S160	20	S157 and network and printer	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2007/12/31 16:26
S161	11943803	(@ad<"20010516" @rlad<"20010516")	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2007/12/31 16:27
S162	19	S154 and S161 and network and printer	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2007/12/31 17:00
S163	1	"20020199026" and bias	US-PGPUB; USPAT; USOCR; EPO; JPO; IBM_TDB	OR	ON	2007/12/31 17:00

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Hardware load balancing through a single fabric - Patent 7184403

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... included in a first intelligent **line card** in a first virtualization server. ... The virtualization engine of claim 11, wherein **load balancing** is used to ...
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[PDF] Microsoft PowerPoint - CS514-fa03-lec07v1.ppt
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Control **cards** periodically. update **line cards** with their own. **load**. • **Line cards** periodically But with **load balancing**, the buckets are the servers... ...
www.cs.cornell.edu/courses/cs514/2003fa/CS514-fa03-lec07v1.pdf - Similar pages

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Pioneer-Standard Gives Intel **Line Card** A Boost ... The products that target **load balancing**, such as the NetStructure 7140 and 7170 Traffic Director products ...
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Device Profile: Peplink Manga **load balancing** multi-**line** access device
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IEEE STD	IEEE Standard

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1 A 20 Gbps Scalable Load Balanced Birkhoff-von Neumann Symmetric TDM Switch IC with SERDES Interfaces

Yu-Hao Hsu, Min-Sheng Kao, Hou-Cheng Tzeng, Ching-Te Chiu, Jen-Ming Wu, Shuo-Hung Hsu

January 2007 **Proceedings of the 2007 conference on Asia South Pacific design automation ASP-DAC '07**

Publisher: IEEE Computer Society

Full text available: pdf(2.70 MB) Additional Information: full citation, abstract

For the first time, we implemented a reconfigurable load-balanced TDM switch IC with SERDES interface circuits for high speed networking applications. An N times N TDM switch could be constructed recursively from the TDM switch IC to achieve switching capacity of hundred gigabits per second or higher. The TDM switch IC contained a digital 8 times 8 TDM switch core with 8B10B CODECs and analog SERDES I/O interfaces. In the I/O interfaces, eight 2.56/3.2Gbps dual-mode 16/20:1 SERDES with CML buffe ...

Keywords: 0.18 micron, Birkhoff-von Neumann symmetric TDM switch IC, SERDES interfaces, load-balanced TDM switch IC, high speed networking, digital TDM switch, 8B10B CODEC, analog SERDES I/O interfaces, dual-mode SERDES, half-rate architectures, all static CMOS gates, low power consumption, wide-band CML buffer, PMOS active load scheme, CMOS technology, 20 Gbit/s

2 Fowarding: Scaling internet routers using optics

Isaac Keslassy, Shang-Tse Chuang, Kyoungsik Yu, David Miller, Mark Horowitz, Olav Solgaard, Nick McKeown

August 2003 **Proceedings of the 2003 conference on Applications, technologies, architectures, and protocols for computer communications SIGCOMM '03**

Publisher: ACM Press

Full text available: pdf(253.06 KB) Additional Information: full citation, abstract, references, citings, index terms

Routers built around a single-stage crossbar and a centralized scheduler do not scale, and (in practice) do not provide the throughput guarantees that network operators need to make efficient use of their expensive long-haul links. In this paper we consider how optics can be used to scale capacity and reduce power in a router. We start with the promising load-balanced switch architecture proposed by C-S. Chang. This approach eliminates the scheduler, is scalable, and guarantees 100% throughput f ...

Keywords: internet router, load-balancing, packet-switch

3 Balancing performance and flexibility with hardware support for network architectures

Ilija Hadžić, Jonathan M. Smith
November 2003 **ACM Transactions on Computer Systems (TOCS)**, Volume 21 Issue 4
Publisher: ACM Press
Full text available: pdf(719.03 KB) Additional Information: full citation, abstract, references, index terms

The goals of performance and flexibility are often at odds in the design of network systems. The tension is common enough to justify an architectural solution, rather than a set of context-specific solutions. The Programmable Protocol Processing Pipeline (P4) design uses programmable hardware to selectively accelerate protocol processing functions. A set of field-programmable gate arrays (FPGAs) and an associated library of network processing modules implemented in hardware are augmented with so ...

Keywords: FPGA, P4, computer networking, flexibility, hardware, performance, programmable logic devices, programmable networks, protocol processing

4 Coyote point equalizer E550si load balancer

Logan G. Harbaugh
March 2007 **Linux Journal**, Volume 2007 Issue 155
Publisher: Specialized Systems Consultants, Inc.
Full text available: html(9.54 KB) Additional Information: full citation, abstract, index terms

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5 Curriculum and content: Service engineering for core optical networks

Lance C. Hassan, Joseph R. Bumblis
October 2003 **Proceedings of the 4th conference on Information technology curriculum CITC4 '03**
Publisher: ACM Press
Full text available: pdf(481.74 KB) Additional Information: full citation, abstract, references, index terms

This article represents a network engineering primer for the next-generation Internet. The authors consider the impact of optical networks in conjunction with Quality of Service (QoS) parameters currently proposed and in use within the public Internet. A primary ingrediant in the successful provisioning of QoS sessions requires traffic engineering techniques and methodologies by automated agents or network engineers. Given the scalability of next-generation routers, as well as the reduced comple ...

Keywords: automatically switched optical networks (ASON), automatically switched transport networks (ASTN), convergence, inverse multiplexing, multiprotocol label switching (MPLS), multiprotocol lambda switching (MP?S), quality of service (QoS), service provisioning, traffic engineering, wavelength division multiplexing (WDM)

6 Operating systems: Platform Overlays: enabling in-network stream processing in large-scale distributed applications

Ada Gavrilovska, Sanjay Kumar, Srikanth Sundaragopalan, Karsten Schwan
June 2005 **Proceedings of the international workshop on Network and operating systems support for digital audio and video NOSSDAV '05**
Publisher: ACM Press

Full text available: pdf(79.42 KB) Additional Information: full citation, abstract, references, citings, index terms

The purpose of this research is to explore the capabilities of future, multi-core heterogeneous systems, with specialized communication support, to be used as efficient and flexible execution platforms in distributed streaming applications. On such platforms, we create overlays of hardware- and software-supported execution contexts -- *platform overlays*. Stream manipulations, represented via stream handlers, are deployed on top of such overlays, based on the ability of individual contexts ...

Keywords: network processors, streaming applications

7 Programmable/extensible networks: Addressing data compatibility on programmable network platforms

Ada Gavrilovska, Karsten Schwan

October 2005 **Proceedings of the 2005 ACM symposium on Architecture for networking and communications systems ANCS '05**

Publisher: ACM

Full text available: pdf(268.88 KB) Additional Information: full citation, abstract, references, index terms

Large-scale applications require the efficient exchange of data across their distributed components, including data from heterogeneous sources and to widely varying clients. Inherent to such data exchanges are (1) discrepancies among the data representations used by sources, clients, or intermediate application components (e.g., due to natural mismatches or due to dynamic component evolution), and (2) requirements to route, combine, or otherwise manipulate data as it is being transferred. As a r ...

Keywords: data morphing, network processors, streaming applications

8 Aslantest: a symbolic execution tool for testing Aslan formal specifications

Jeffrey Douglas, Richard A. Kemmerer

August 1994 **Proceedings of the 1994 ACM SIGSOFT international symposium on Software testing and analysis ISSTA '94**

Publisher: ACM Press

Full text available: pdf(995.16 KB) Additional Information: full citation, abstract, references, citings, index terms, review

This paper introduces Aslantest, a symbolic execution tool for the formal specification language Aslan. Aslan is a state-based specification language built on first-order predicate calculus with equality. Aslantest animates Aslan specifications and enables users to interactively run specific test cases or symbolically execute the specification. Testing the formal specifications early in the software life cycle allows one to assure a reliable system that also provides the desired functionali ...

9 A parallel, incremental, mostly concurrent garbage collector for servers

Katherine Barabash, Ori Ben-Yitzhak, Irit Goft, Elliot K. Kolodner, Victor Leikehman, Yoav Ossia, Avi Owshanko, Erez Petrank

November 2005 **ACM Transactions on Programming Languages and Systems (TOPLAS)**, Volume 27 Issue 6

Publisher: ACM Press

Full text available: pdf(683.50 KB) Additional Information: full citation, abstract, references, citings, index terms

Multithreaded applications with multigigabyte heaps running on modern servers provide new challenges for garbage collection (GC). The challenges for "server-oriented" GC include: ensuring short pause times on a multigigabyte heap while minimizing throughput

penalty, good scaling on multiprocessor hardware, and keeping the number of expensive multicycle fence instructions required by weak ordering to a minimum.We designed and implemented a collector facing these demands building on th ...

Keywords: Garbage collection, JVM, concurrent garbage collection

10 A two-tier heterogeneous mobile Ad Hoc network architecture and its load-balance routing problem

Chi-Fu Huang, Hung-Wei Lee, Yu-Chee Tseng
August 2004 **Mobile Networks and Applications**, Volume 9 Issue 4
Publisher: Kluwer Academic Publishers
Full text available: pdf(1.28 MB) Additional Information: full citation, abstract, references, index terms

The mobile ad hoc network (MANET) has attracted a lot of interest recently. However, most of the existing works have assumed a stand-alone MANET. In this paper, we propose a two-tier, heterogeneous MANET architecture which can support Internet access. The low tier of the network consists of a set of mobile hosts each equipped with a IEEE 802.11 wireless LAN card. In order to connect to the Internet and handle the network partitioning problem, we propose that the high tier is comprised of a subse ...

Keywords: ad hoc network, load balance, mobile computing, routing, wireless network

11 Load balancing for multi-projector rendering systems

Rudrajit Samanta, Jiannan Zheng, Thomas Funkhouser, Kai Li, Jaswinder Pal Singh
July 1999 **Proceedings of the ACM SIGGRAPH/EUROGRAPHICS workshop on Graphics hardware HWWS '99**
Publisher: ACM Press
Full text available: pdf(1.79 MB) Additional Information: full citation, references, citings, index terms

Keywords: cluster computing, immersive display systems, load balancing, parallel rendering

12 GPGPU: general purpose computation on graphics hardware

David Luebke, Mark Harris, Jens Krüger, Tim Purcell, Naga Govindaraju, Ian Buck, Cliff Woolley, Aaron Lefohn
August 2004 **ACM SIGGRAPH 2004 Course Notes SIGGRAPH '04**
Publisher: ACM Press
Full text available: pdf(63.03 MB) Additional Information: full citation, abstract, citings

The graphics processor (GPU) on today's commodity video cards has evolved into an extremely powerful and flexible processor. The latest graphics architectures provide tremendous memory bandwidth and computational horsepower, with fully programmable vertex and pixel processing units that support vector operations up to full IEEE floating point precision. High level languages have emerged for graphics hardware, making this computational power accessible. Architecturally, GPUs are highly parallel s ...

13 Scheduling computations on a software-based router

Xiaohu Qie, Andy Bavier, Larry Peterson, Scott Karlin
June 2001 **ACM SIGMETRICS Performance Evaluation Review , Proceedings of the 2001 ACM SIGMETRICS international conference on Measurement and modeling of computer systems SIGMETRICS '01**, Volume 29 Issue 1

Publisher: ACM Press
Full text available: pdf(1.46 MB) Additional Information: full citation, abstract, references, citings

Recent efforts to add new services to the Internet have increased the interest in software-based routers that are easy to extend and evolve. This paper describes our experiences implementing a software-based router, with a particular focus on the main difficulty we encountered: how to schedule the router's CPU cycles. The scheduling decision is complicated by the desire to differentiate the level of service for different packet flows, which leads to two fundamental conflicts: (1) assigning proce ...

14 A simplified approach to web service development
Peter M. Kelly, Paul D. Coddington, Andrew L. Wendelborn
January 2006 **Proceedings of the 2006 Australasian workshops on Grid computing and e-research - Volume 54 ACSW Frontiers '06**
Publisher: Australian Computer Society, Inc.
Full text available: pdf(132.83 KB) Additional Information: full citation, abstract, references, index terms

Most languages used for developing web services and clients exhibit properties which make calling remote functions across a network a non-trivial task. The type systems used by object oriented languages have many incompatibilities with those required for service interfaces, and the complicated tasks of generating proxy objects and WSDL service definitions mean that a lot of effort is required to create a service, in comparison with defining classes and functions to be used locally. We discuss th ...

15 Fairness and load balancing: Fairness and load balancing in wireless LANs using association control
Yigal Bejerano, Seung-Jae Han, Li (Erran) Li
September 2004 **Proceedings of the 10th annual international conference on Mobile computing and networking MobiCom '04**
Publisher: ACM Press
Full text available: pdf(377.05 KB) Additional Information: full citation, abstract, references, citings, index terms

Recent studies on operational wireless LANs (WLANs) have shown that user load is often unevenly distributed among wireless access points (APs). This unbalanced load results in unfair bandwidth allocation among users. We observe that the unbalanced load and unfair bandwidth allocation can be greatly alleviated by intelligently associating users to APs, termed *association control*, rather than having users greedily associate APs of best received signal strength.In this study, we present an e ...

Keywords: IEEE 802.11, approximation algorithms, load balancing, max-min fairness, wireless local area networks (WLAN)

16 A TCAM-based distributed parallel IP lookup scheme and performance analysis
Kai Zheng, Chengchen Hu, Hongbin Lu, Bin Liu
August 2006 **IEEE/ACM Transactions on Networking (TON)**, Volume 14 Issue 4
Publisher: IEEE Press
Full text available: pdf(883.08 KB) Additional Information: full citation, abstract, references, index terms

Using ternary content addressable memory (TCAM) for high-speed IP address lookup has been gaining popularity due to its deterministic high performance. However, restricted by the slow improvement of memory accessing speed, the route lookup engines for next-generation terabit routers demand exploiting parallelism among multiple TCAM chips. Traditional parallel methods always incur excessive redundancy and high power consumption. We propose in this paper an original TCAM-based IP lookup scheme tha ...

Keywords: IP, TCAM, power consumption, route lookup, throughput

17 The CIRRUS banking network

David Gifford, Alfred Spector
August 1985 **Communications of the ACM**, Volume 28 Issue 8
Publisher: ACM Press
Full text available: pdf(1.27 MB) Additional Information: full citation, abstract, citings, index terms, review

The CIRRUS banking network makes coast-to-coast automatic banking transactions possible. The system will soon be able to handle international currency transactions and point-of-sale transactions in stores.

18 Real-time shading

Marc Olano, Kurt Akeley, John C. Hart, Wolfgang Heidrich, Michael McCool, Jason L. Mitchell, Randi Rost
August 2004 **ACM SIGGRAPH 2004 Course Notes SIGGRAPH '04**
Publisher: ACM Press
Full text available: pdf(7.39 MB) Additional Information: full citation, abstract

Real-time procedural shading was once seen as a distant dream. When the first version of this course was offered four years ago, real-time shading was possible, but only with one-of-a-kind hardware or by combining the effects of tens to hundreds of rendering passes. Today, almost every new computer comes with graphics hardware capable of interactively executing shaders of thousands to tens of thousands of instructions. This course has been redesigned to address today's real-time shading capabili ...

19 Operating system principles

Per Brinch Hansen
January 1973 Book
Publisher: Prentice-Hall, Inc.
Full text available: pdf(16.81 MB) Additional Information: full citation, abstract, references, cited by, index terms

From the Preface

MAIN GOAL

This book tries to give students of computer science and professional programmers a general understanding of *operating systems*--the programs that enable people to share computers efficiently.

To make the sharing of a computer tolerable, an operating system must enforce certain rules of behavior on all its users. One would therefore expect the designers of operating systems to do their utmost to make them as s ...

20 A parallel, incremental and concurrent GC for servers

Yoav Ossia, Ori Ben-Yitzhak, Irit Goft, Elliot K. Kolodner, Victor Leikehman, Avi Owshanko
May 2002 **ACM SIGPLAN Notices , Proceedings of the ACM SIGPLAN 2002 Conference on Programming language design and implementation PLDI '02**, Volume 37 Issue 5
Publisher: ACM Press
Full text available: pdf(231.80 KB) Additional Information: full citation, abstract, references, citings, index terms

Multithreaded applications with multi-gigabyte heaps running on modern servers provide

new challenges for garbage collection (GC). The challenges for "server-oriented" GC include: ensuring short pause times on a multi-gigabyte heap, while minimizing throughput penalty, good scaling on multiprocessor hardware, and keeping the number of expensive multi-cycle fence instructions required by weak ordering to a minimum. We designed and implemented a fully parallel, incremental, mostly concurrent colle ...

Keywords: JVM, Java, concurrent garbage collection, garbage collection, incremental garbage collection, weak ordering

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